

Program: Electronics and Telecommunication Engineering

Curriculum Scheme: Revised 2012

Examination: Second Year

Semester IV

**Course Code: ETC403 Course Name: Microprocessors and
Peripherals**

Time: 1 hour

Max. Marks: 50

Note: All the questions are compulsory and carry equal marks.

Q.1	8085 Microprocessor is a
Option A:	16 bit microprocessor
Option B:	32 bit microprocessor
Option C:	8 bit microprocessor
Option D:	64 bit microprocessor
Q.2	Memory addressing capacity of 8085 is
Option A:	1MB
Option B:	64KB
Option C:	1024 Byte
Option D:	16KB
Q.3	No of Address lines available in 8086 Address Bus is
Option A:	16 Address lines
Option B:	18 Address lines
Option C:	20 Address lines
Option D:	24 Address lines
Q.4	Maximum size of one Memory Segment of 8086 microprocessor
Option A:	1MB
Option B:	64KB
Option C:	1024 Byte
Option D:	16KB
Q.5	How many 8 bit ports are there in 8255
Option A:	1 Port
Option B:	2 Port
Option C:	3 Port
Option D:	4 Port

Q.6	How many addressing lines are required to interface 32KB RAM chip with 8086 microprocessor
Option A:	15 address lines
Option B:	16 address lines
Option C:	14 address lines
Option D:	18 address lines
Q.7	Choose correct instruction for Register addressing mode
Option A:	MOV AX, [BX+SI+04H]
Option B:	MOV AX, [BX+SI]
Option C:	MOV AX, [BX]
Option D:	MOV AX, BX
Q.8	ADC 0809 is having how many analog input channels
Option A:	2 channels
Option B:	4 channels
Option C:	8 channels
Option D:	16 channels
Q.9	Maximum how many segment are active in 8086
Option A:	4
Option B:	8
Option C:	16
Option D:	32
Q.10	If DS = 8000H and BP = 0200H, choose correct physical address
Option A:	82000H
Option B:	8200H
Option C:	80200H
Option D:	80020H
Q.11	TF flag is used to
Option A:	Disable Interrupts
Option B:	For single step execution of program
Option C:	To set the direction of counter
Option D:	To indicate overflow
Q.12	Choose the assembler directive from the given choices
Option A:	ENDS
Option B:	IN
Option C:	MOVSW
Option D:	LEA
Q.13	To interface ADC or DAC with 8086 microprocessor, we required
Option A:	8255

Option B:	8259
Option C:	8257
Option D:	8254
Q.14	To calculate address of the next instruction to be execute following register pair is used
Option A:	SS: BP
Option B:	DS: BX
Option C:	CS: IP
Option D:	ES: DI
Q.15	Operating principle of stack memory of 8086 is
Option A:	First In First Out (FIFO)
Option B:	Last In First Out (LIFO)
Option C:	Last In Last Out (LILO)
Option D:	First In Last Out (FILO)
Q.16	How many dedicated or Predefined Interrupts are there in 8086
Option A:	255
Option B:	5
Option C:	27
Option D:	224
Q.17	Addressing capacity of 80286
Option A:	1MB
Option B:	64MB
Option C:	1024 MB
Option D:	16MB
Q.18	80286 supports
Option A:	Real Addressing mode
Option B:	Protected virtual address mode
Option C:	Both (A) and (B)
Option D:	Only Protected virtual address mode
Q.19	In conditional Jump instruction target must be within
Option A:	- 128 to +127 bytes from first byte of next instruction
Option B:	- 1024 to +1023 bytes from first byte of next instruction
Option C:	- 32767 to +32766 bytes from first byte of next instruction
Option D:	Anywhere in 1MB memory
Q.20	CALL instruction
Option A:	Save return address on stack
Option B:	Save return address and Flag register on stack
Option C:	Save Flag register on stack

Option D:	Do not save any return address
Q.21	which of the following file extension is used to save Assembly language file
Option A:	.EXE
Option B:	.ASM
Option C:	.OBJ
Option D:	.DOC
Q.22	CLK signal is required in
Option A:	Only DAC
Option B:	both DAC and ADC
Option C:	only ADC
Option D:	not available in ADC and DAC
Q.23	While interfacing memory with 8086 Address line A0 is connected to
Option A:	Output Enable pin of EVEN BANK memory chip
Option B:	Chip Select pin of EVEN BANK memory chip
Option C:	Chip Select pin of ODD BANK memory chip
Option D:	Output Enable pin of ODD BANK memory chip
Q.24	while interfacing 8286 (Bidirectional buffer) with 8086
Option A:	DT/R is connected to T and DEN is connected to OE
Option B:	DEN is connected to T and DT/R is connected to OE
Option C:	only DT/R is connected to OE
Option D:	only DEN is connected to T
Q.25	How many operating modes are there for 8255 PPI
Option A:	Three
Option B:	Four
Option C:	Eight
Option D:	Two