



Mahavir Education Trust's
Shah & Anchor Kutchhi Engineering College, Chembur, Mumbai 400088

UG Programme in Electronics Engineering
TIME TABLE FOR INTERNAL ASSESMENT(IA) KT EXAM

DATE:02/05/2021

DATE	TIME	SE		TE		BE	
		SEM 3	SEM 4	SEM 5	SEM 6	SEM 7	SEM8(CBGS)
18/05/2021	11:00 to 11:30AM	AM-III(CBCS) EM III(C Sch)		DLIC(CBCS)		ISD(CBCS)	
	1:00 to 1:30 PM	-	AM-IV(CBCS)		SS(CBCS)		CMOS VLSI D.
	2:00 to 2:30 PM	EDC-I(CBCS) EDC-I(C Sch)		MCA(CBCS)		PE(CBCS)	
	3:00 to 3:30 PM	-	EDC-II(CBCS)		ES & R(CBCS)		ANT
19/05/2021	11:00 to 11:30AM	ENAS(CBCS) ENAS(C Sch)	-	DBMS(CBCS)		ANT (CBCS) /ROBO(CBCS)/NNFL(CBCS)	
	1:00 to 1:30 PM	-	MPA(CBCS)		CCN(CBCS)		MEMS TECH.
	2:00 to 2:30 PM	EIM(CBCS) EIM(C Sch)	-	EEM(CBCS)		DSP(CBCS)	
	3:00 to 3:30 PM	-	DSD(CBCS)				ROBO/MOB.COMM.
20/05/2021	11:00 to 11:30AM	DCD(CBCS) DLC(C Sch)	-	DCOM(CBCS)		CS & L(CBCS)/MIS(CBCS)	
	1:00 to 1:30 PM	-	LCS(CBCS)		WCOM(CBCS) ./COA(CBCS)		
	2:00 to 2:30 PM	OOPM(CBCS) OOPM (C Sch)	-				



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	3:00 to 3:30 PM		PCE(CBCS)		VLSID(CBCS) Nibha D.		
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COE:

HOD :

PRINCIPAL: