



Mahavir Education Trust's  
Shah & Anchor Kutchhi Engineering College, Chembur,  
Mumbai 400088

**UG Programme in Electronics Engineering**  
**TIME TABLE FOR INTERNAL ASSESSMENT II**

**DATE: 31/10/2020**

Date	Time	SE	TE	BE
19-11-2020	11:30 am to 12:00 pm	Electrical Network Analysis and Synthesis	Engineering Electromagnetics	Advance Networking Technologies/ Neural Network and Fuzzy Logic/Integrated Circuit Technology
	4:00 pm to 4:30 pm	Electronic Instruments and Measurements	Database Management Systems	Power Electronics
20-11-2020	11:30 am to 12:00 pm	Digital Logic Circuits	Design with Linear Integrated Circuits	Management Information System /Operation Research
	4:00 pm to 4:30 pm	Object Oriented Programming Methodology	-	-
21-11-2020	11:30 am to 12:00 pm	Electronics Devices & Circuit I	Microcontrollers and Applications	Digital Signal Processing
	4:00 pm to 4:30 pm	Engineering Mathematics III	Digital Communication	Instrumentation System Design

HOD:

COE:

PRINCIPAL: