



How to reach?



**Shah and Anchor Kutchhi Engineering
College**

W. T. Patil Marg

Near Duke's Company

Chembur, Mumbai – 400088

PATRONS

Shri Keshavjibhai Chhadva
Chairman, Mahavir Education Trust

Shri Mansukhbhai Shah
Trustee & Hon. Joint Secretary

Shri Premjibhai Rambhiya
Hon. Joint Secretary

Shri Navinbhai Shah,
Managing Trustee

Dr. (Mrs.) Uma R. Rao
I/C Principal

CONVENOR

Dr. (Mrs.) Uma R. Rao,
*In-charge Principal &
Head, Department of Electronics Engineering*

COORDINATORS

Prof. (Mrs.) Sushma Srivastava
Electronics Engg. Dept.
Contact: 9821254462
E-mail: sushsri2012@gmail.com

Prof. (Mrs.) Sarika Bukkavar
Electronics Engg. Dept.
Contact: 9819669962
E-mail: sarika_bukkavar@hotmail.com

Please address all communications to

Dr. (Mrs.) Uma R. Rao,
*In-charge Principal and
Head, Electronics Engineering Department*
Shah and Anchor Kutchhi Engg. College

**ISTE Approved One Week
STTP**

On

***An Insight into VLSI
and Nanotechnology***

July 14 – 18, 2014

Organized by

**Department of Electronics Engineering
Shah & Anchor Kutchhi Engg. College,
Chembur,
Mumbai - 400 088**



Website: www.shahandanchor.com

ABOUT SAKEC

Shah and Anchor Kutchhi Engineering College was established in 1984 for the purpose of imparting quality technical education. The college is managed by Mahavir Education Trust. It offers under graduate courses and Post graduate courses in Electronics Engineering, Computer Engineering and Information Technology, an undergraduate course in Electronics and Telecommunication Engineering and a post graduate course in Management Studies. The college is approved by AICTE, New Delhi and Government of Maharashtra. It is also an ISO:9001-2008 certified institute.

VISION AND MISSION

We at SAKEC cherish a mission of achieving our goal of imparting state of art technical education in emerging engineering disciplines. We fulfill our commitment through periodic review of academic environment and management system and continual improvement of infrastructure, teaching techniques and faculty skills to keep pace with the technological developments. While doing so we ensure compliance with applicable rules and regulations.

COURSE STRUCTURE

This course consists of two major components: lectures and lab sessions. The course is aiming at providing an insight to the subjects of VLSI Design and Nanotechnology. As these subjects are prescribed by University of Mumbai for several branches of engineering, there is constant need to

train faculty in these subjects. Apart from the syllabus, an introduction to the current fields of applications of VLSI and Nanotechnology is presented. Laboratory session is conducted for the participants to get them acquainted with TCAD simulation tools in SILVACO software. A visit to Nanoscience and Nanotechnology centre at Mumbai University, Kalina is planned for the participants to get an overview of the facilities available at the center.

COURSE CONTENTS

The course covers the following topics:

- Analog CMOS VLSI circuit design
- Radiation harness in semiconductors
- VLSI reliability issues
- MEMs Technology
- Introduction to Nanotechnology
- Nanoelectronics and Nanodevices
- Nano-biosensors

RESOURCE PERSONS

The course will be conducted by eminent speakers from premier R and D institutions like IITB, BARC and other reputed engineering colleges.

REGISTRATION FEE

Since the course is organized on self financed basis each participant will be charged a course fee of Rs. 2500/-. This fee includes visit to Nanoscience and Nanotechnology centre

(Mumbai University), course material, lunch and tea.

Last date for receipt of application in prescribed format is 7th July 2014.

REGISTRATION FORM

Name:

Designation:

Institution:

Address:

Phone:

E-mail:

ISTE Membership No.:

Details of payments:

(DD or cheque should be drawn in favor of Shah and Anchor Kutchhi Engineering College, Chembur, payable at Mumbai.)

DD/Cheque No.:

Bank:

Date:

Signature of participant:

Signature with seal of the sponsoring authority: