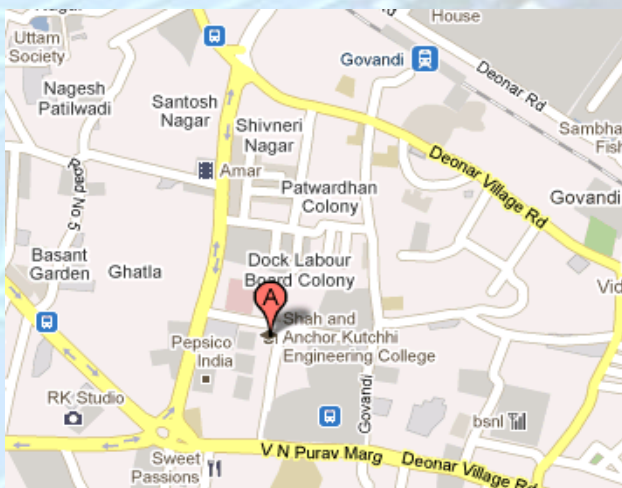




How to Reach?



Shah & Anchor Kutchhi Engineering College
W.T. Patil Marg,
Near Dukes Company,
Chembur, Mumbai 400 088

PATRONS

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Mahavir Education Trust's Shah & Anchor Kutchhi Engineering College



**IETE Approved One week
STTP**

On

***“Advanced VLSI Design
and Applications with
hands-on in CADENCE
toolset”***

25th to 30th June 2018

ORGANIZED BY
**Department of Electronics &
Telecommunication Engineering**
In association with



Entuple Technologies Pvt. Ltd.
Bangalore

**Shah & Anchor Kutchhi Engineering
College, Chembur, Mumbai 400 088**
www.shahandanchor.com
Website: www.shahandanchor.com

ABOUT SAKEC

Shah and Anchor Kutchhi Engineering College was established in 1985 for the purpose of imparting quality technical education. The college is managed by Mahavir Education Trust. It offers under graduate courses in Electronics Engineering, Computer Engineering, Information Technology and Electronics and Telecommunication Engineering. The college also offers post graduate courses in Electronics Engineering, Computer, and Information Technology. The college is approved by AICTE, New Delhi and Government of Maharashtra. It is also an ISO-9001-2015 certified institute.

VISION AND MISSION

We at SAKEC cherish a mission of achieving our goal of imparting state of art technical education in emerging Engineering disciplines. We fulfill our commitment through periodic review of academic environment and management system and continual improvement of infrastructure, teaching techniques and faculty skills to keep pace with the technological developments. While doing so we ensure compliance with applicable rules and regulations.

COURSE STRUCTURE

This course will mainly cover challenges of Analog and mixed signal VLSI design over different applications. It will also focus on the digital VLSI design flow for specific applications.

The course is aiming at providing a hands – on session on CADENCE tools, which is the most used tool for chip designing by leading VLSI industries.

COURSE CONTENTS

The course covers the following topics –

- 3-days exclusive Hands-on sessions on CADENCE tool
- RF CMOS VLSI
- Verification
- System Design using FPGA
- Data Converters
- Full Custom Design
- ASIC Design
- Applications of VLSI

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Dr. Jayanta Mukherjee, Professor, IITB
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Mrs. Sushma Srivastava, Associate Professor, SAKEC
Mr. Sumit Patil, Sr. Application Engineer, Entuple Technologies

COURSE FEE

Participant	Fees
Faculty/Industry Candidates	3000
Students/Research Scholars	1000

Last date for receipt of application in prescribed format is 20th June 2018.
Seats limited to 40 participants

REGISTRATION FORM

Name – _____

Designation – _____

Institution – _____

Address – _____

Phone – _____

Email – _____

IETE Membership No. - _____

Details of payments – _____

Cheque should be drawn in favor of “Shah and Anchor Kutchhi Engineering College, Chembur”, payable at Mumbai.

DD / Cheque No. _____

Bank _____

Date _____

Signature of participant: _____

Signature with seal of the sponsoring authority

For online registration (Mandatory) click below

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